

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Canceled)

PN 1. ~~2.~~ (Currently Amended) ~~The method of controlling output from a power amplifier as claimed in claim 1~~ A method of controlling output from a power amplifier which has a driver stage and an output stage, the method comprising:
detecting a first and a second electrical parameter at an output of the output stage,
wherein the first electrical parameter is an electrical current;
processing the first parameter with a first reference signal to generate a bias control signal;
processing the second parameter with a second reference signal to generate a bias reduction signal;
feeding the bias control signal to an input of both the driver and the output stages; and
feeding the bias reduction signal to an input of the driver stage.

2. ~~2.~~ (Currently Amended) ~~The method of controlling output from a power amplifier as claimed in claim 1~~ A method of controlling output from a power amplifier which has a driver stage and an output stage, the method comprising:
detecting a first and a second electrical parameter at an output of the output stage,
wherein the second electrical parameter is an output voltage envelope;
processing the first parameter with a first reference signal to generate a bias control signal;
processing the second parameter with a second reference signal to generate a bias reduction signal;
feeding the bias control signal to an input of both the driver and the output stages; and

feeding the bias reduction signal to an input of the driver stage.

4. - 8. (Canceled)

PN 3 ~~8.~~ (New) A method of controlling output from a power amplifier which has a driver stage and an output stage, the method comprising:
detecting a first and a second electrical parameter at an output of the output stage, wherein the first electrical parameter is an electrical current and the second electrical parameter is an output voltage envelope;
processing the first parameter with a first reference signal to generate a bias control signal;
processing the second parameter with a second reference signal to generate a bias reduction signal;
feeding the bias control signal to an input of both the driver and the output stages: and
feeding the bias reduction signal to an input of the driver stage.

- ⁴ ~~10~~. (New) An electrical circuit comprising:
a power amplifier, having a driver stage and an output stage;
an external control loop; and
a protection circuit, having:

a detection means coupled to the output of the output stage for detecting a first and a second electrical parameter at an output of the power amplifier, wherein said first electrical parameter is an electrical current; and

a bias reduction means coupled to the input of the driver stage for providing a bias signal at an input of the power amplifier and a reference signal generator coupled to the bias reduction means.

- ⁵ ~~11~~. (New) The electrical circuit of Claim ⁴ ~~10~~, for use in a mobile telecommunications device.

- ⁶ ~~12~~. (New) An electrical circuit comprising:
a power amplifier, having a driver stage and an output stage;
an external control loop; and
a protection circuit, having:

a detection means coupled to the output of the output stage for detecting a first and a second electrical parameter at an output of the power amplifier, wherein the second electrical parameter is an output voltage envelope; and

a bias reduction means coupled to the input of the driver stage for providing a bias signal at an input of the power amplifier and a reference signal generator coupled to the bias reduction means.

- ⁷ ~~13~~. (New) The electrical circuit of Claim ⁶ ~~12~~, for use in a mobile telecommunications device.

⁸ ~~14~~. (New) An electrical circuit comprising:

a power amplifier, having a driver stage and an output stage;
an external control loop; and
a protection circuit, having:

PN a detection means coupled to the output of the output stage for detecting a first and a second electrical parameter at an output of the power amplifier, wherein the first electrical parameter is an electrical current and the second electrical parameter is an output voltage envelope; and

a bias reduction means coupled to the input of the driver stage for providing a bias signal at an input of the power amplifier and a reference signal generator coupled to the bias reduction means.

⁹ ~~15~~. (New) The electrical circuit of Claim ⁸ ~~14~~, for use in a mobile telecommunications device.